Basic Electrical Engineering: Network Analysis

*Required

1. Email address *

2. Name *

3. Branch *

Mark only one oval.

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4. Roll No *

Section 2

All questions are necessary

5. Any closed path through two or more elements of the network in the network 1 point is called a ______.*

Mark only one oval.

BranchNodeLinear path

- ____ Loop
- 6. Potential difference between two nodes is termed as ______across 1 point the elements connected between the two nodes. *

Mark only one oval.

\square	Current
\square	Reference
\square	Link
\square) Voltage

 Voltage dependent current source is the source whose voltage depends upon 1 point the______ through some element other specified part of the circuit. *

Mark only one oval.

- Current linearity
- Power
 - 📄 Voltage

8. A network is said to be Linear if it satisfies the following conditions: *

1 point

Mark only one oval.

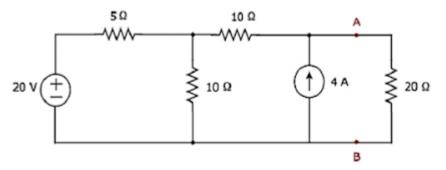
C Linearity

linearity and Additivity

Additivity

- Homogeneity and Additivity
- 9. The current through 5 Ohm resister is *

1 point



Mark only one oval.

2 A 0.4 A 13/2 A

None of the above

10. while simplifying the circuit with the help of Norton's Theorem the Large part 1 point of the network is replaced by an equivalent ______ source in Parallel with Norton's Equivalent ______.*

Mark only one oval.

📃 Voltage, Resistance

Current, Resistance

Current, Voltage

None of the above

11. While simplifying the circuit with the help of Thevenin's Theorem the Larger 1 point part of the network is replaced by an equivalent ______ source in Series with Thevenin's Equivalent ______.*

Mark only one oval.

\square) Voltage, Resistance
\square) Current, Resistance
\square) Current, Voltage
\square	None of the above

12. While simplifying the circuit with the help of Thevenin's/ Norton's Theorem 1 point the Thevenin's /Norton's Equivalent Resistance is measured by removing all the ideal sources. In this case the ideal voltage source is ______ circuited and Ideal current source is ______ circuited. *

Mark only one oval.

Short, Short

Open, Short

Short, Open

Open, Open

13. _____ Theorem is a test to test the linearity of the circuit. In other 1 point way it is applicable to the _____ circuits only *

Mark only one oval.

Reciprocity, Linearity

Linearity, Linearity

Superposition, Linear

- Maximum Power transfer, Resistive
- 14. The power supplied by Ideal dc Voltage Source is ______ when 1 point maximum power condition is achuieved and maximum power delivered to the load resister is 250 W. *

Mark only one oval.

250 W
500 W
750 W
Canot be calculated with above data

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